

**Listing of Claims:**

The following is a listing of claims in this application:

1. – 20. (cancelled)

21. (previously presented) A thin film transistor (TFT) array panel for a liquid crystal display, comprising:

a first line;

a second line intersecting the first line;

a pixel region defined by the intersection of the second line and the first line;

a pixel electrode formed in the pixel region; and,

a ladder-shaped conductive pattern connected to the second line and having a pair of parallel, spaced-apart riser-like elements respectively disposed on opposite sides of the first line.

22. (previously presented) The TFT array panel of claim 21, further comprising a storage wire including a storage electrode line.

23. (previously presented) The TFT array panel of claim 21, further comprising a passivation layer formed between the pixel electrode and the first line and the second line.

24. (previously presented) A thin film transistor (TFT) array panel for a liquid crystal display, comprising:

a first line;

a common wire including a common electrode line;

a second line intersecting the first line;

a pixel region defined by the intersection of the first line and the second line;

a pixel electrode formed in the pixel region and overlapping the common electrode; and,

a ladder-shaped conductive pattern connected to the second line and having a pair of parallel, spaced-apart riser-like elements respectively disposed on opposite sides of the first line.

25. – 30. (cancelled)

31. (previously presented) The TFT array panel of claim 22, wherein the conductive pattern and the storage wire are formed in the same layer.